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PATENT APPLICATION

HEWLETT-PACKARD COMPANY Intellectual Property Administration P.O. Box 272400 Fort Collins, Colorado 80527-2400

ATTORNEY DOCKET NO.

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

lan P. Shaeffer et al.

Confirmation No.: 4331

Application No.: 10/654,177

Examiner: Jonathan J. Johnson

September 3, 2003 Filing Date:

Group Art Unit: 1725

Title: A METHOD OF FABRICATING A SUBSTANTIALLY ZERO SIGNAL DEGRADATION ELECTRICAL

CONNECTION ON A PRINTED CIRCUIT BOARD

**Commissioner For Patents** PO Box 1450 Alexandria, VA 22313-1450

## PETITION FOR EXTENSION OF TIME

ffice Action mailed on	February 7, 2006	on the above-identified U.S. P.	atent application, a shortened
y period of3 m	onths was set for respons	se. In accordance with 37 CFR	t 1.136(a), applicant(s) hereby
(s) a:			
one month	two month	three month	four month
eation to charge the fe tal letter. At any time of yment to Deposit Account to 08-2025 under 37 Cl	e required by 37 CFR 1 turing the pendency of the ant 08-2025 pursuant to 3 FR 1.16 through 1.21 inc	1.17 to Deposit Account 08-20 is application, please charge a 17 CFR 1.25. Additionally please clusive, and any other section	ny fees required or credit any se charge any fees to Deposit
eposited with the United S First Class Mail Express Mail Service of Express Mail Label Not Date of Deposit In an envelope addressed to commissioner for Patents, of thereby certify that this patensmitted to the Patent a facsimile number (571) 27: Date of facsimile: June 7. Typed Name  Patrick G: Signature:	tates Postal Service as ander 37 CRF 1.10 .: o: Alexandria, VA 22313-1450 R per is being and Trademark Office 3-8300. 2006	, tog	r Applicant(s) ,080 ne 7, 2006
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